



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

## \*BIBDATASHEET\*

CONFIRMATION NO. 1468

Bib Data Sheet

SERIAL NUMBER 10/677,012	FILING DATE 10/02/2003  RULE	CLASS 365	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 67161-112
-----------------------------	---------------------------------------	--------------	------------------------	----------------------------------

## APPLICANTS

Yasunobu Nakase, Hyogo, JAPAN;

## \*\* CONTINUING DATA

## \*\* FOREIGN APPLICATIONS

JAPAN 2003-002366(P), 01/08/2003

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/03/2004

Foreign Priority claimed

☒ yes ☐ no

35 USC 119 (a-d) conditions met

☒ yes ☐ no

Verified and Acknowledged

Examiner's Signature

Initials

STATE OR

COUNTRY  
JAPAN

SHEETS

DRAWING  
18

TOTAL

CLAIMS  
9

INDEPENDENT

CLAIMS  
1

## ADDRESS

McDermott, Will &amp; Emery

600 13th Street, N.W.

Washington, DC

20005-3096

## TITLE

Semiconductor memory device having potential amplitude of global bit line pair restricted to partial swing

FILING FEE  RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees ( Filing )
		<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
		<input type="checkbox"/> 1.18 Fees ( Issue )
		<input type="checkbox"/> Other _____
		<input type="checkbox"/> Credit